



Image

2814
#1
PATENT
8040-1036

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Masamoto TAGO et al.

Confirmation No. 7977

Serial No. 10/000,020

GROUP 2814

Filed December 4, 2001

Examiner Dilinh P. Nguyen

METHOD FOR LAMINATING AND MOUNTING
SEMICONDUCTOR CHIP

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 22, 2004

Sir:

The undersigned hereby petitions for an extension of time to respond to the Official Action of November 24, 2003 for one month to March 24, 2004.

Please charge the extension fee of \$110 to Deposit Account No. 25-0120. If this fee is insufficient, the Patent Office is hereby authorized to charge any additional extension fee to Deposit Account No. 25-0120. A duplicate copy of this sheet is enclosed.

A responsive paper is filed herewith.

Respectfully submitted,

YOUNG & THOMPSON

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